

Refine Search

Search Results -

Terms	Documents
L1.clm.	6

Database:

- US Pre-Grant Publication Full-Text Database
- US Patents Full-Text Database
- US OCR Full-Text Database
- EPO Abstracts Database
- JPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

Search:

L2

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Friday, October 07, 2005 [Printable Copy](#) [Create Case](#)

Set Name Query
side by side

Hit Count Set Name
result set

DB=PGPB; PLUR=YES; OP=OR

<u>L2</u>	L1.clm.	6	<u>L2</u>
<u>L1</u>	(address\$3 near5 auto\$6) same multiplex\$3	72	<u>L1</u>

END OF SEARCH HISTORY

Freeform Search

Database:

US Pre-Grant Publication Full-Text Database

US Patents Full-Text Database

US OCR Full-Text Database

EPO Abstracts Database

JPO Abstracts Database

Derwent World Patents Index

IBM Technical Disclosure Bulletins

Term:

(address\$3 near3 auto\$6) same multiplex\$3 same

bus same register

Display:

10

Documents in Display Format:

-

Starting with Number

1

Generate:

☐ Hit List

☒ Hit Count

☐ Side by Side

☐ Image

Search

Clear

Interrupt

Search History

DATE: Friday, October 07, 2005 [Printable Copy](#) [Create Case](#)

<u>Set Name</u> <u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
side by side		result set
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR		
L1 (address\$3 near3 auto\$6) same multiplex\$3 same bus same register	30	L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(address\$3 near3 auto\$6) same multiplex\$3 same bus same register	2

Database:

- US Pre-Grant Publication Full-Text Database
- US Patents Full-Text Database
- US OCR Full-Text Database
- EPO Abstracts Database
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Search:

L2

Refine Search

Recall Text

Clear

Interrupt

Search History

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Set Name Query
side by side

Hit Count Set Name
result set

DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L2 (address\$3 near3 auto\$6) same multiplex\$3 same bus same register

2 L2

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L1 (address\$3 near3 auto\$6) same multiplex\$3 same bus same register

30 L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(700/24 709/245 370/254 370/471 370/457 370/475 710/104 710/110 710/9 710/62 710/3 379/221.14 340/825.52 340/3.5).ccls.	7142

Database:

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- JPO Abstracts Database
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- IBM Technical Disclosure Bulletins

Search:

L3

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Friday, October 07, 2005 [Printable Copy](#) [Create Case](#)

<u>Set Name</u> side by side	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u> result set
	<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=OR</i>		
<u>L3</u>	710/104,110,9,62,3;370/254,471,457,475;340/825.52,3.5;379/221.14;700/24;709/245.ccls.	7142	<u>L3</u>
	<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<u>L2</u>	(address\$3 near3 auto\$6) same multiplex\$3 same bus same register	2	<u>L2</u>
	<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=OR</i>		
<u>L1</u>	(address\$3 near3 auto\$6) same multiplex\$3 same bus same register	30	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L1 and L3	3

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L4

Search History

 DATE: Friday, October 07, 2005 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=OR</i>			
<u>L4</u>	11 and L3	3	<u>L4</u>
<u>L3</u>	710/104,110,9,62,3;370/254,471,457,475;340/825.52,3.5;379/221.14;700/24;709/245.ccls.	7142	<u>L3</u>
<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L2</u>	(address\$3 near3 auto\$6) same multiplex\$3 same bus same register	2	<u>L2</u>
<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=OR</i>			
<u>L1</u>	(address\$3 near3 auto\$6) same multiplex\$3 same bus same register	30	<u>L1</u>

END OF SEARCH HISTORY

EAST - [Untitled1:1]

File View Edit Tools Window Help

☐ Drafts
☐ Pending
☒ Active
 L1: (56) (address\$3 nea
 L2: (19) 11 same regist
☐ Failed
☐ Saved
☐ Favorites
☐ Tagged (0)
☐ UDC
☐ Queue
☐ Trash

Search

DBs: USPAT ☒ Plurals
 Default operator: OR ☒ Highlight all hit terms initially

11 same register

	U	I	Document ID	Issue Dat	Pages	Title	Current OR	Current X
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6919756 B2	20050719	14	Method and circuit for adjusting a voltage upo	327/536	
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6839831 B2	20050104	35	Data processing apparatus with register	712/218	712/219
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6778003 B1	20040817	12	Method and circuit for adjusting a voltage upo	327/536	
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6556208 B1	20030429	19	Network management card for use in a system for	345/520	710/107; 710/260
5	<input type="checkbox"/>	<input type="checkbox"/>	US 5872945 A	19990216	11	MX bus translation to new system bus protocol	710/315	710/100; 710/104
6	<input type="checkbox"/>	<input type="checkbox"/>	US 5784382 A	19980721	62	Method and apparatus for dynamically testing	714/726	714/718; 714/732;
7	<input type="checkbox"/>	<input type="checkbox"/>	US 5440705 A	19950808	17	Address modulo adjust unit for a memory manag	711/220	711/201
8	<input type="checkbox"/>	<input type="checkbox"/>	US 5333288 A	19940726	13	Effective address pre-calculation type pi	711/214	711/221
9	<input type="checkbox"/>	<input type="checkbox"/>	US 4935867 A	19900619	16	Signal processor memory management unit with in	711/217	708/491
10	<input type="checkbox"/>	<input type="checkbox"/>	US 4888778 A	19891219	20	Algebraic coder-decoder for Reed Solomon and BC	714/755	
11	<input type="checkbox"/>	<input type="checkbox"/>	US 4879646	19891107	35	Data processing system	712/227	714/38;



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Search Results

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Results for "((address*<in>metadata) <and> (auto*<in>metadata))<and> (multiplex*&l..."

Your search matched 13 of 1242336 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.

e-mail print friendly

» Search Options

[View Session History](#)[New Search](#)

Modify Search

☐ Check to search only within this results set

 Display Format: ☒ Citation ☐ Citation & Abstract

» Key

IEEE JNL. IEEE Journal or Magazine

IEE JNL. IEE Journal or Magazine

IEEE CNF. IEEE Conference Proceeding

IEE CNF. IEE Conference Proceeding

IEEE STD. IEEE Standard

Select Article Information

- ☐ 1. **EZ encoding: a class of Irredundant low power codes for data address and multiplexed address buses**
 Aghaghiri, Y.; Fallah, F.; PEDram, M.;
 Design, Automation and Test in Europe Conference and Exhibition, 2002. Proceedings
 4-8 March 2002 Page(s):1102
 Digital Object Identifier 10.1109/DATE.2002.998458
[AbstractPlus](#) | Full Text: [PDE\(241 KB\)](#) IEEE CNF
- ☐ 2. **Society of Automotive Engineers AS4074 family high-speed, fault-tolerant data communications standards for integrated avionics**
 Meyer, J.W.;
 Digital Avionics Systems Conference, 1990. Proceedings., IEEE/AIAA/NASA 9th
 15-18 Oct. 1990 Page(s):651 - 656
 Digital Object Identifier 10.1109/DASC.1990.111362
[AbstractPlus](#) | Full Text: [PDE\(440 KB\)](#) IEEE CNF
- ☐ 3. **Society of Automotive Engineers AS-2 standardization activities**
 Meyer, J.W.;
 Aerospace and Electronics Conference, 1992. NAECON 1992., Proceedings of the IEEE 1992 National
 18-22 May 1992 Page(s):1076 - 1082 vol.3
 Digital Object Identifier 10.1109/NAECON.1992.220632
[AbstractPlus](#) | Full Text: [PDE\(468 KB\)](#) IEEE CNF
- ☐ 4. **Design and evaluation of an architecture for a digital signal processor for instrumentation applications**
 Fellman, R.D.; Kaneshiro, R.T.; Konstantinides, K.;
 Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing], IEEE Transactions on
 Volume 38, Issue 3, March 1990 Page(s):537 - 546
 Digital Object Identifier 10.1109/29.106871
[AbstractPlus](#) | Full Text: [PDE\(896 KB\)](#) IEEE JNL
- ☐ 5. **The introduction of networking into the automotive industry**
 Holland, R.;
 New Product Introduction in Electronics (Ref. No. 2000/047), IEE Seminar
 12 April 2000 Page(s):3/1 - 316
[AbstractPlus](#) | Full Text: [PDE\(540 KB\)](#) IEE CNF
- ☐ 6. **Architecture and model of Profinet IO**
 Poschraann, A.; Neumann, P.;
 AFRICON, 2004. 7th AFRICON Conference in Africa
 Volume 2, 2004 Page(s):1213 - 1218 Vol.2
 Digital Object Identifier 10.1109/AFRICON.2004.1406884
[AbstractPlus](#) | Full Text: [PDE\(697 KB\)](#) IEEE CNF

- ☐ 7. **LOTTERYBUS: a new high-performance communication architecture for system-on-chip designs**
Lahiri, K.; Raghunathan, A.; Lakshminarayana, G.;
Design Automation Conference, 2001. Proceedings
2001 Page(s):15 - 20
[AbstractPlus](#) | Full Text: [PDF](#)(760 KB) IEEE CNF
- ☐ 8. **An ASIC library granular DRAM macro with built-in self test**
Dreibelbis, J.; Barth, J., Jr.; Kho, R.; Kalter, T.;
Solid-State Circuits Conference, 1998. Digest of Technical Papers. 45th ISSCC 1998 IEEE International
5-7 Feb. 1998 Page(s):74 - 75, 416
Digital Object Identifier 10.1109/ISSCC.1998.672381
[AbstractPlus](#) | Full Text: [PDF](#)(888 KB) IEEE CNF
- ☐ 9. **Radiated emissions testing of an experimental electric vehicle**
Gaul, H.W.; Huettl, T.; Powers, C.;
Electromagnetic Compatibility, 1996. Symposium Record. IEEE 1996 International Symposium on
19-23 Aug. 1996 Page(s):338 - 342
Digital Object Identifier 10.1109/IEMC.1996.561254
[AbstractPlus](#) | Full Text: [PDF](#)(480 KB) IEEE CNF
- ☐ 10. **Fault tolerance in safety critical automotive applications: cost of agreement as a limiting factor**
Poledna, S.;
Fault-Tolerant Computing, 1995. FTCS-25. Digest of Papers., Twenty-Fifth International Symposium on
27-30 June 1995 Page(s):73 - 82
Digital Object Identifier 10.1109/FTCS.1995.466996
[AbstractPlus](#) | Full Text: [PDF](#)(904 KB) IEEE CNF
- ☐ 11. **BAM2: a vehicle network for economy-model automobiles**
dos Reis Filho, C.A.; da Silva, E.P., Jr.; de Lima Azevedo, E.; Seminario, J.A.P.; Dibb, L.;
Devices, Circuits and Systems, 1998. Proceedings of the 1998 Second IEEE International Caracas Conference on
2-4 March 1998 Page(s):98 - 101
Digital Object Identifier 10.1109/ICCDSCS.1998.705814
[AbstractPlus](#) | Full Text: [PDF](#)(396 KB) IEEE CNF
- ☐ 12. **Low power techniques for address encoding and memory allocation**
Wei-Chung Cheng; Pedram, M.;
Design Automation Conference, 2001. Proceedings of the ASP-DAC 2001. Asia and South Pacific
30 Jan.-2 Feb. 2001 Page(s):245 - 250
Digital Object Identifier 10.1109/ASPDAC.2001.913313
[AbstractPlus](#) | Full Text: [PDF](#)(476 KB) IEEE CNF
- ☐ 13. **Testability, debuggability, and manufacturability features of the UltraSPARC-I microprocessor**
Levitt, M.E.; Nori, S.; Narayanan, S.; Grewal, G.P.; Youngs, L.; Jones, A.; Billus, G.; Paramanandam, S.;
Test Conference, 1995. Proceedings., International
21-25 Oct. 1995 Page(s):157 - 166
Digital Object Identifier 10.1109/TEST.1995.529829
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